

10/555614

JCO6 Rec'd PCT/PTO 04 NOV 2005

03500.109594

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)	
	:	Examiner: Not Yet Assigned
DAISUKE MIURA ET AL.	)	
	:	TC/Art Unit: Not Yet Assigned
Application No.: Not Yet Assigned	)	
(National Phase of Int'l Application No.	:	
PCT/JP2005/004586 filed 03/09/05)	)	
	:	
Filed: Concurrently Herewith	)	
	:	
For: FIELD EFFECT TRANSISTOR,	)	
METHOD OF PRODUCING THE	:	
SAME, AND METHOD OF	)	
PRODUCING LAMINATED	:	
MEMBER	)	November 3, 2005

Mail Stop: PCT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to examination on the merits and prior to calculating the filing fee,  
please amend the above-identified application, filed herewith, as follows:

- Amendments to the claims are presented in the listing starting on page 2; and
- Remarks are presented on page 15.